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Applicant(s): Anderson et al.

Docket No.

BUR920040170US1

Application No.  
10/711,425

Filing Date  
9/17/2004

Examiner  
Owens, Douglas W.

Group Art Unit  
2811

Invention: ISOLATION STRUCTURES IN SEMICONDUCTOR INTEGRATED CIRCUITS (IC)

I hereby certify that this Request for Reconsideration (10 pages)  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Anderson, *et al.*

Examiner: Owens, Douglas W.

Serial No.: 10/711,425

Art Unit: 2811

Filing Date: 09/17/2004

Docket No.: BUR920040170US1

Title: **ISOLATION STRUCTURES IN SEMICONDUCTOR INTEGRATED CIRCUITS  
(IC)**

Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Request for Reconsideration**

Sir:

This Request for Reconsideration is in response to the Office Action mailed June 30, 2005.

10/711,425

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